#### Foundations and Realization of Open, Accessible Design

Andrew B. Kahng, UCSD

abk-openroad@ucsd.edu

https://theopenroadproject.org

https://github.com/The-OpenROAD-Project





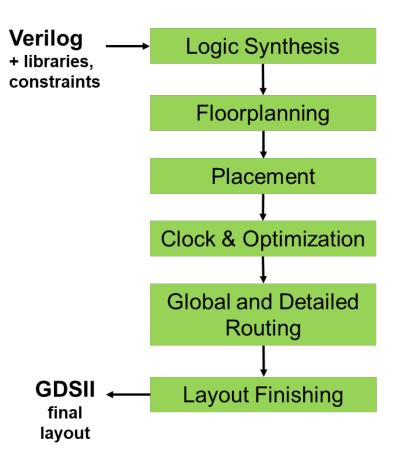




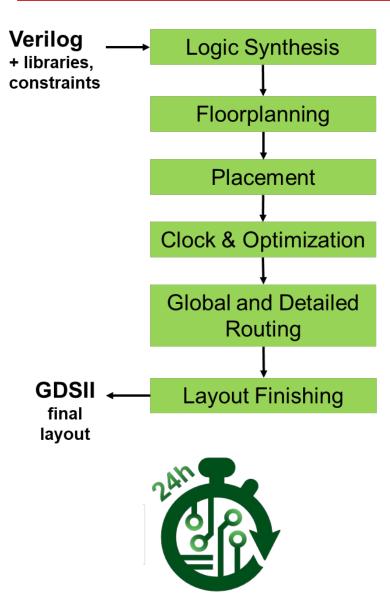




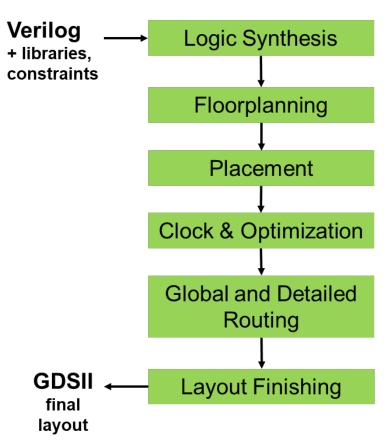










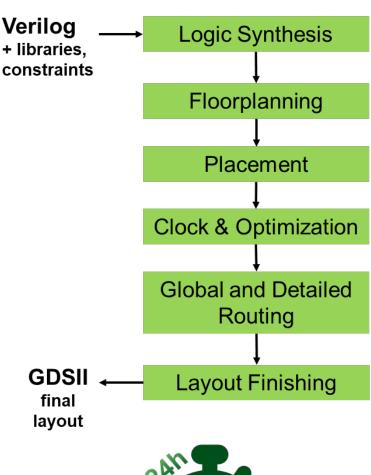


- Functionality: 600+ tapeouts, 130-12nm
- Community: OpenROAD app has 16K commits from 77 contributors
- Education and Workforce: from high school to graduate level, extension









- Functionality: 600+ tapeouts, 130-12nm
- **Community:** OpenROAD app has 16K commits from 77 contributors
- Education and Workforce: from high school to graduate level, extension



- Researchers
- Small R&D teams, startups







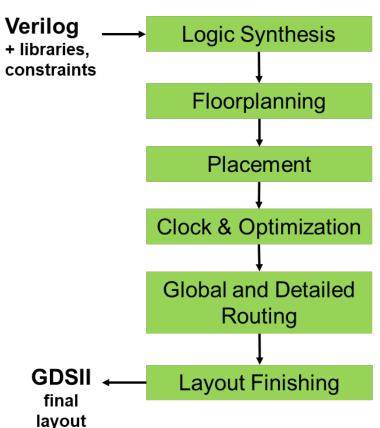














- Functionality: 600+ tapeouts, 130-12nm
- **Community:** OpenROAD app has 16K commits from 77 contributors
- Education and Workforce: from high school to graduate level, extension

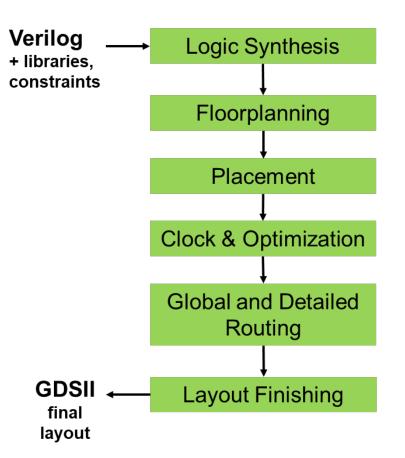


- Researchers
- Small R&D teams, startups





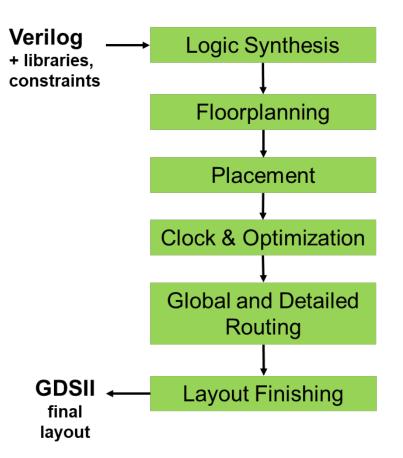
### **Directions:**



• What if tool licenses are unlimited?



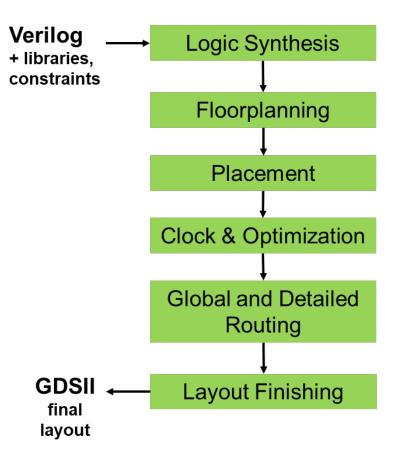
# **Directions: Cloud, ML**



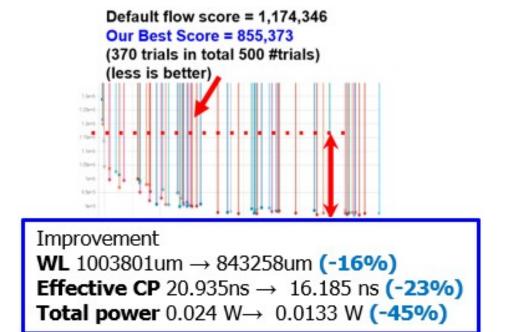
- What if tool licenses are unlimited?
  "COPILOT" = Cloud Optimized Physical Implementation using OpenROAD Technology
- ML challenge: predict failure and intervene



# **Directions: Cloud, ML**

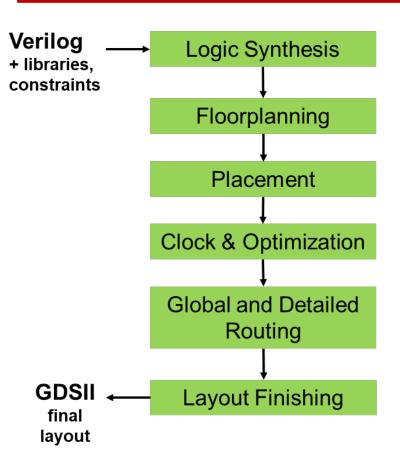


- What if tool licenses are unlimited?
  "COPILOT" = Cloud Optimized Physical Implementation using OpenROAD Technology
- ML challenge: predict failure and intervene
- + low-hanging fruits such as AutoTuner





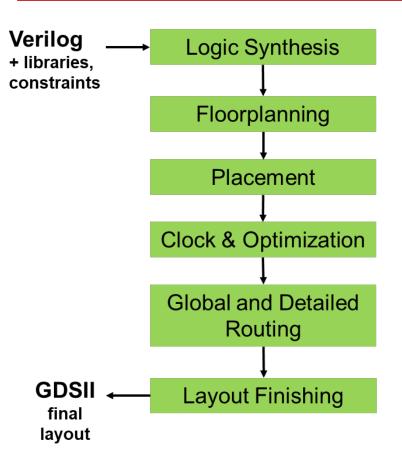
# **Directions: Early Design Space Exploration**



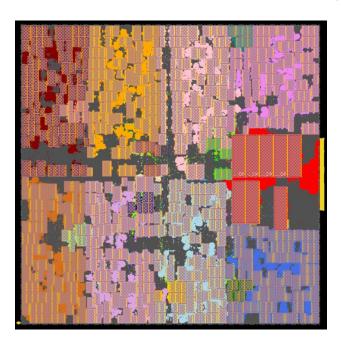
 Can we better explore architecture and SoC floorplan design spaces?



# **Directions: Early Design Space Exploration**

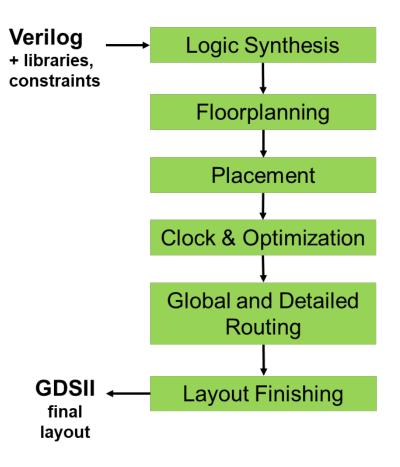


- Can we better explore architecture and SoC floorplan design spaces?
- Hier-RTLMP: /src/mpl2
  - RTL and dataflow-driven, human expert-like

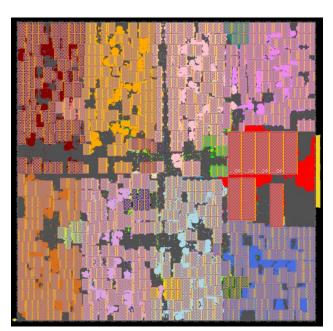




# **Directions: Early Design Space Exploration**

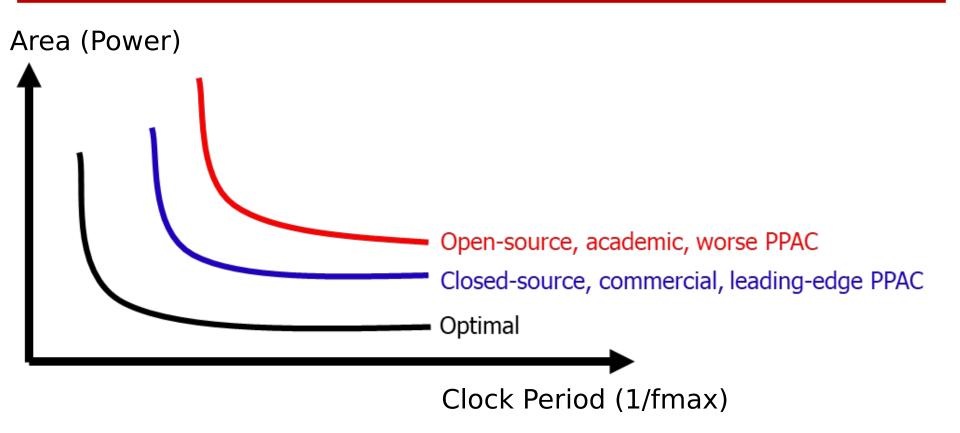


- Can we better explore architecture and SoC floorplan design spaces?
- Hier-RTLMP: /src/mpl2
  - RTL and dataflow-driven, human expert-like

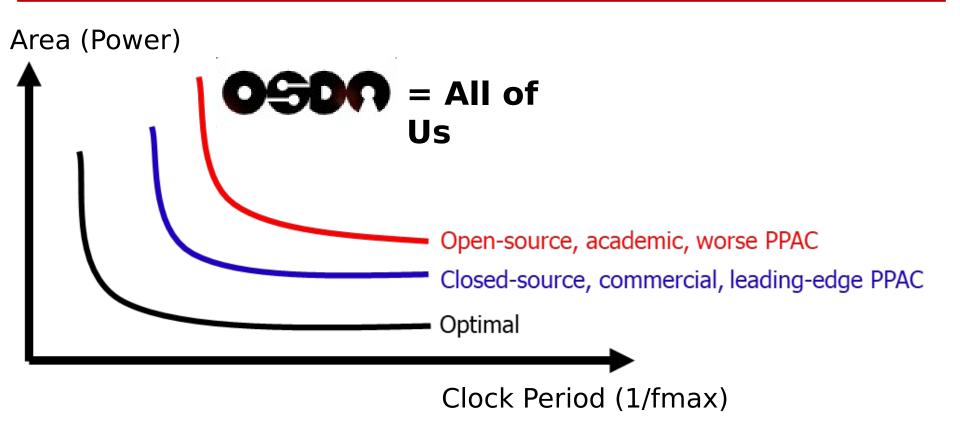


- TritonPart: /src/par
  - Timing- and constraint-driven partitioner
  - Displaces hMETIS, KaHyPar

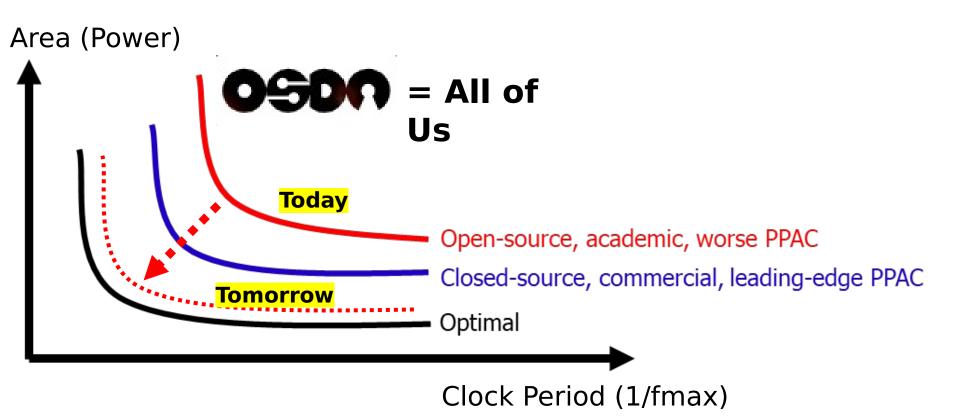




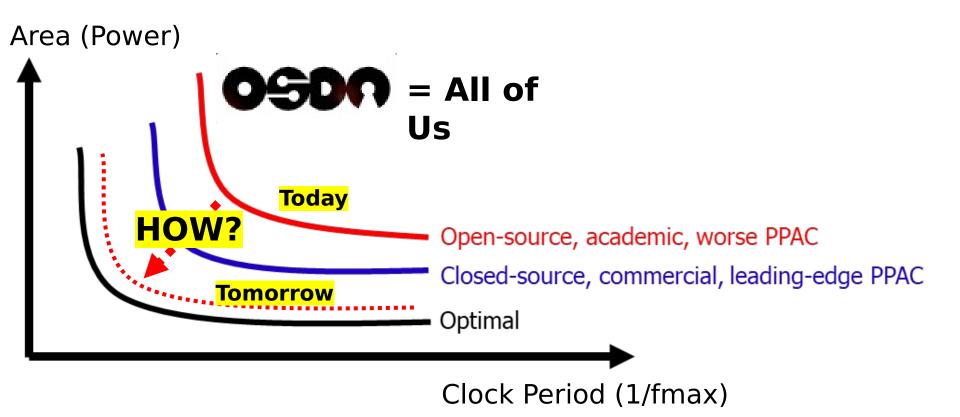














Area (Power) = All of **Today** Open-source, academic, worse PPAC Closed-source, commercial, leading-edge PPAC Tomorrow Optimal

Clock Period (1/fmax)

- Engines: do better
- Cloud: exploit 1000s of runs
- Machine Learning: generate and share data, models



Area (Power) = All of **Today** Open-source, academic, worse PPAC Closed-source, commercial, leading-edge PPAC Tomorrow Optimal

Clock Period (1/fmax)

- Engines: do better
- Cloud: exploit 1000s of runs
- Machine Learning: generate and share data, models

Efficiency ... as a Community!



# **Thought #1: Bars Matter**

#### Is it good enough?

- Relevance (functionality, data, quality of results)
  - Foundry N5 vs. <u>ASAP7</u> / <u>ASAP5</u>
  - Commercial SP&R (PPAC) vs. academic/open-source SP&R
  - Commercial product designs vs. open hardware designs
- Quality, continuous improvement
- Support



# **Thought #1: Bars Matter**

### Is it good enough?

- Relevance (functionality, data, quality of results)
  - Foundry N5 vs. <u>ASAP7</u> / <u>ASAP5</u>
  - Commercial SP&R (PPAC) vs. academic/open-source SP&R
  - Commercial product designs vs. open hardware designs
- Quality, continuous improvement
- Support

## • Can I rely on it?

- Ph.D. students vs. design/EDA professionals
- Documented, maintained, supported
- User community
- Availability, terms and conditions



# **Thought #1: Bars Matter**

### Is it good enough?

- Relevance (functionality, data, quality of results)
  - Foundry N5 vs. <u>ASAP7</u> / <u>ASAP5</u>
  - Commercial SP&R (PPAC) vs. academic/open-source SP&R
  - Commercial product designs vs. open hardware designs
- Quality, continuous improvement
- Support

### Can I rely on it?

- Ph.D. students vs. design/EDA professionals
- Documented, maintained, supported
- User community
- Availability, terms and conditions

"more wood behind fewer arrows"?



- Claim: Infrastructure is not differentiating
  - This means Pick one, move on



- Claim: Infrastructure is not differentiating
  - This means Pick one, move on
- Data model, DB, incremental STA, readers/writers, standards support, PDK support, logging, scripting, parallelization, GUI ... should be like plumbing and utilities
  - "Don't need to think about these" is also a Bar!



- Claim: Infrastructure is not differentiating
  - This means Pick one, move on
- Data model, DB, incremental STA, readers/writers, standards support, PDK support, logging, scripting, parallelization, GUI ... should be like plumbing and utilities
  - "Don't need to think about these" is also a Bar!

#### \*See:

- OpenDB
- METRICS2.1
- OpenROAD
- •

"roadbed for the road ahead"

#### A road starts with a roadbed

At the nexus of learning, optimization, and CAD, several foundational elements provide a "roadbed" for the road ahead. These include: 1) benchmarking and roadmapping of CAD/EDA optimizations; 2) data to enable data-driven methods; and 3) "EDA 2.0" that broadly reinvents core optimization algorithms and tool architectures for scalability on modern compute substrates.

doi:10.1109/MDAT.2022.3161593



- Claim: Infrastructure is not differentiating
  - This means Pick one, move on
- Data model, DB, incremental STA, readers/writers, standards support, PDK support, logging, scripting, parallelization, GUI ... should be like plumbing and utilities
  - "Don't need to think about these" is also a Bar!

#### \*See:

- OpenDB
- METRICS2.1
- OpenROAD
- •

"roadbed for the road ahead"

#### A road starts with a roadbed

At the nexus of learning, optimization, and CAD, several foundational elements provide a "roadbed" for the road ahead. These include: 1) benchmarking and roadmapping of CAD/EDA optimizations; 2) data to enable data-driven methods; and 3) "EDA 2.0" that broadly reinvents core optimization algorithms and tool architectures for scalability on modern compute substrates.

"more wood behind fewer arrows"!

doi:10.1109/MDAT.2022.3161593



- What else blocks adoption of open-source EDA?
  - Validations (of relevance)



- What else blocks adoption of open-source EDA?
  - Validations (of relevance)
  - Root-cause blocker: "not sharable"



- What else blocks adoption of open-source EDA?
  - Validations (of relevance)
  - Root-cause blocker: "not sharable"
- Ex: foundry N5 PDKs, enablements are not sharable
  - grow <u>ASAP7</u> / <u>ASAP5</u> PDKs, enablements into **research proxies**
- Ex: commercial EDA SP&R tools/IP are not sharable
  - can grow OpenROAD, etc. into research proxies
- Ex: commercial IPs are not sharable
  - can grow NVDLA, OpenPiton, Chipyard, etc. into research proxies



- What else blocks adoption of open-source EDA?
  - Validations (of relevance)
  - Root-cause blocker: "not sharable"
- Ex: foundry N5 PDKs, enablements are not sharable
  - grow <u>ASAP7</u> / <u>ASAP5</u> PDKs, enablements into **research proxies**
- Ex: commercial EDA SP&R tools/IP are not sharable
  - can grow OpenROAD, etc. into research proxies
- Ex: commercial IPs are not sharable
  - can grow NVDLA, OpenPiton, Chipyard, etc. into research proxies
- Improve proxies to unblock ourselves!



- What else blocks adoption of open-source EDA?
  - Validations (of relevance)
  - Root-cause blocker: "not sharable"
- Ex: foundry N5 PDKs, enablements are not sharable
  - grow <u>ASAP7</u> / <u>ASAP5</u> PDKs, enablements into **research proxies**
- Ex: commercial EDA SP&R tools/IP are not sharable
  - can grow OpenROAD, etc. into research proxies
- Ex: commercial IPs are not sharable
  - can grow NVDLA, OpenPiton, Chipyard, etc. into research proxies
- Improve proxies to unblock ourselves!

"A journey of a thousand miles begins with a single step"



- Efficiency is the biggest challenge for open-source EDA
  - Here to discuss and learn: how to be a better part of the OSDA community, how to achieve more with the people we have



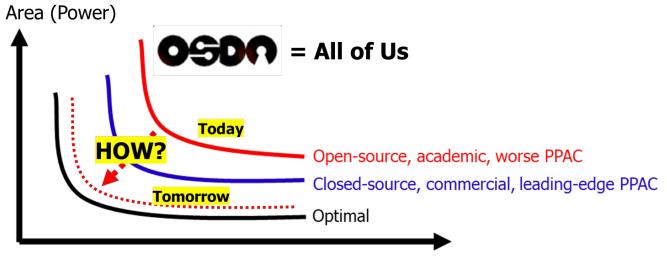
- Efficiency is the biggest challenge for open-source EDA
  - Here to discuss and learn: how to be a better part of the OSDA community, how to achieve more with the people we have
- Bars matter: critical mass, critical quality
- Infrastructure is not differentiating: pick one, move on
- Not sharable is always a blocker: keep improving proxies



- Efficiency is the biggest challenge for open-source EDA
  - Here to discuss and learn: how to be a better part of the OSDA community, how to achieve more with the people we have
- Bars matter: critical mass, critical quality
- Infrastructure is not differentiating: pick one, move on
- Not sharable is always a blocker: keep improving proxies
- What will be the lasting outcomes of this workshop?
  - Collaboration, synergy, efficiency, "more wood behind fewer arrows", ... ?



- Efficiency is the biggest challenge for open-source EDA
  - Here to discuss and learn: how to be a better part of the OSDA community, how to achieve more with the people we have
- Bars matter: critical mass, critical quality
- Infrastructure is not differentiating: pick one, move on
- Not sharable is always a blocker: keep improving proxies
- What will be the lasting outcomes of this workshop?
  - Collaboration, synergy, efficiency, "more wood behind fewer arrows", ... ?





#### **Some Links**

(talks are always posted at visicad.ucsd.edu)

- "Leveling Up: A Trajectory of OpenROAD, TILOS and Beyond", Proc. ISPD, March 2022. <a href="https://pptx">.pptx</a>
- "Our Real Scaling Challenge: People", ACCESS-CEDA seminar, Sept. 2022.
  <u>pptx</u>
- "Bars and Barriers to Overcome for Shared ML EDA Infrastructure", NSF Workshop on Shared Infrastructure for Machine Learning EDA, March 2023.
   <a href="mailto:pptx">.pptx</a>
- Thoughts on open source in EDA
  - 2002: "Toward CAD-IP Reuse: The MARCO GSRC Bookshelf of Fundamental CAD Algorithms" [.pdf] (also: [.pdf])
  - 2019: "Looking Into the Mirror of Open Source" [.pdf]
  - 2020: "Open-Source EDA: If We Build It, Who Will Come?" [.pdf]
  - 2021: "The OpenROAD Project: Unleashing Hardware Innovation" [.pdf]
  - 2022: "The OpenROAD Project: A Foundation for Research and Education in EDA and IC Design" [.pptx]
  - 2022: A. B. Kahng, "A Mixed Open-Source and Proprietary EDA Commons for Education and Prototyping", [
     <u>.pdf</u>] [.pptx]
- <u>"https://theopenroadproject.org</u> and <u>https://github.com/The-OpenROAD-Project</u>



# THANK YOU!



