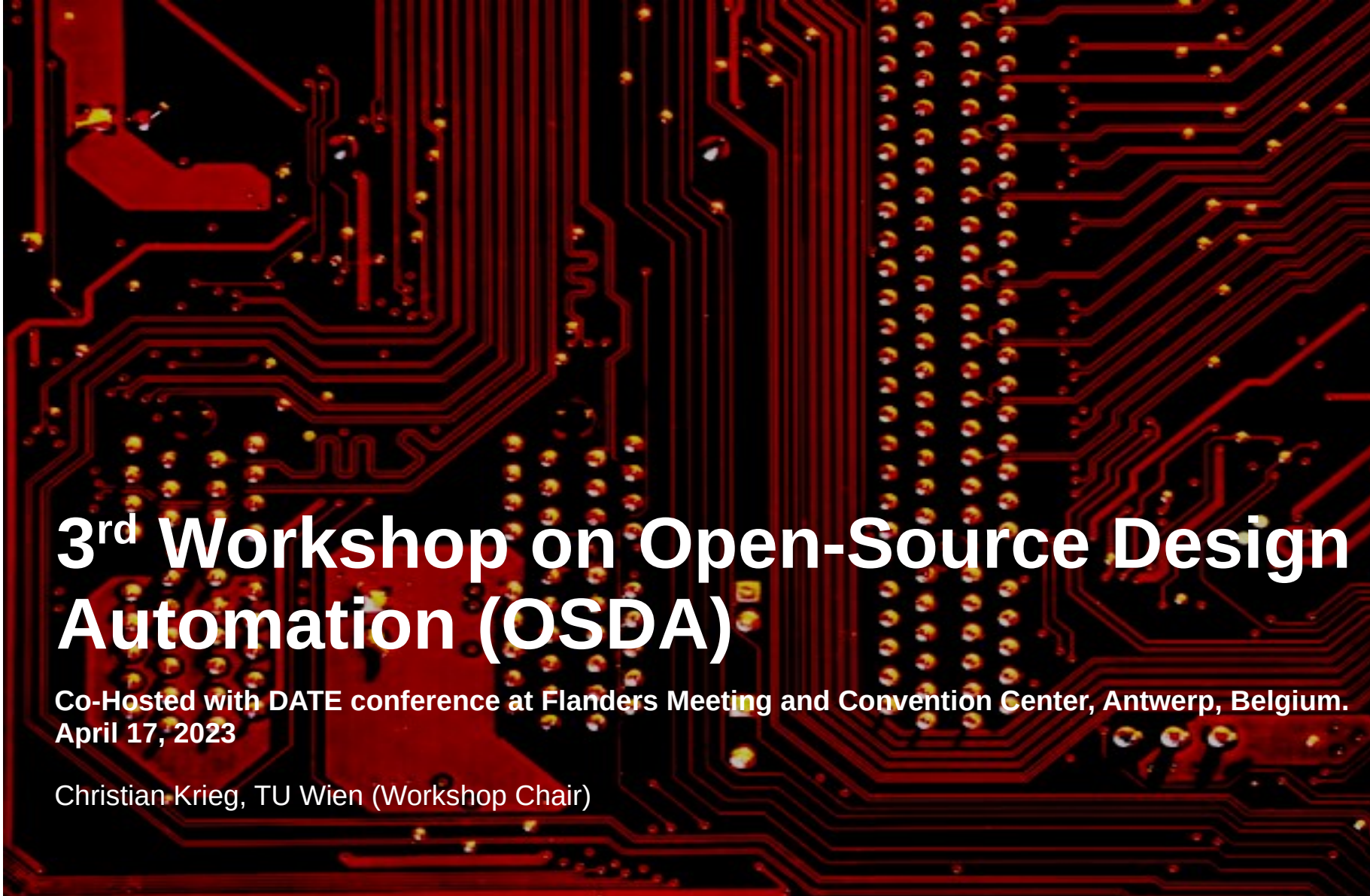

3rd Workshop on Open-Source Design Automation (OSDA)

Co-Hosted with DATE conference at Flanders Meeting and Convention Center, Antwerp, Belgium.
April 17, 2023

Christian Krieg, TU Wien (Workshop Chair)

The Workshop

THANK YOU!

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Andrea Borga
CEO and CTO at oliscience

Andrew B. Kahng
Professor at University of California San Diego

Antonino Tumeo
Chief scientist at Pacific Northwest National Laboratory (PNNL)

Christian Krieg (Chair)
Post-Doc at TU Wien

Claire Xenia Wolf
CTO at YosysHQ

Daniel Grosse
Full professor at Johannes Kepler University Linz & DFKI Bremen

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Senior Firmware Engineer at University of Birmingham

Frans Skarman
PhD Student at Linköping University

Jean-Paul Chaput
Engineer at Sorbonne Université

Jim Lewis
OSVVM Architect at SynthWorks

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Senior Scientist/Engineer at Lawrence Berkeley National Labs

Lucas Klemmer
PhD student at JKU Linz

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Myrtle Shah
PhD student at Heidelberg University

Nima Taherinejad
Full Professor at University of Heidelberg

Oscar Gustafsson
Associate Professor at Linköping University

Rishiyur Nikhil
Co-founder and CTO at Bluespec Inc.

Stefan Riesenberger
Master Student at TU Wien (Vienna University of Technology)

Steffen Reith
Professor at RheinMain University of Applied Sciences

Steve Hoover
CEO at Redwood EDA

Tim Edwards
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Tristan Gingold
HDL Developer at CERN

Tsung-Wei Huang
Assistant Professor at University of Utah

Vamsi Vytla
Electronics Engineer at Lawrence Berkeley National Lab

Xin Fang
Qualcomm

... to all the people that made OSDA happen!

THANK YOU!

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Workshop	Registrations
W04 (OSDA)	213
WXX	183
WXX	171
WXX	128
WXX	118
WXX	99

... to our audience!

Christian Krieg <christian@osda.ws>

THANK YOU!

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THANK YOU!

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... to DATE conference organization for putting up such a nice conference, and for the great support!

Christian Krieg <christian@osda.ws>

The workshop program

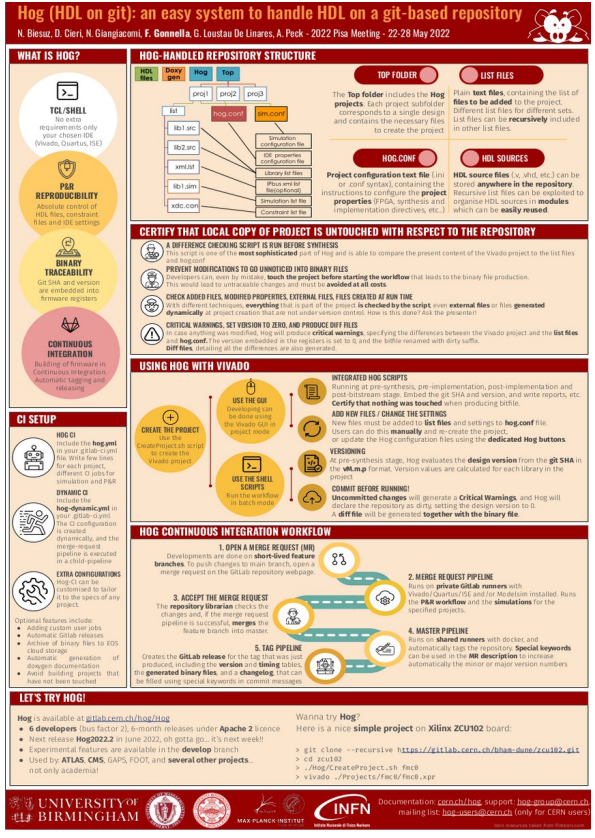
- Check out the program at the workshop website: https://osda.ws/program
- Sign up for Newsletter **NOW!** Follow us on Social media!
- 12 invited speakers
- 1 submitted presentation
- 4 posters (in the rear of the room)
- Unfortunately, 2 invited speakers were not able to come
  (we will provide recordings of them at the workshop website)
- The program is VEEERY DENSE
- We need a lot of disciplince with regard to time: 10 min talk, 5 min Q&A

Christian Krieg <christian@osda.ws>

The Posters

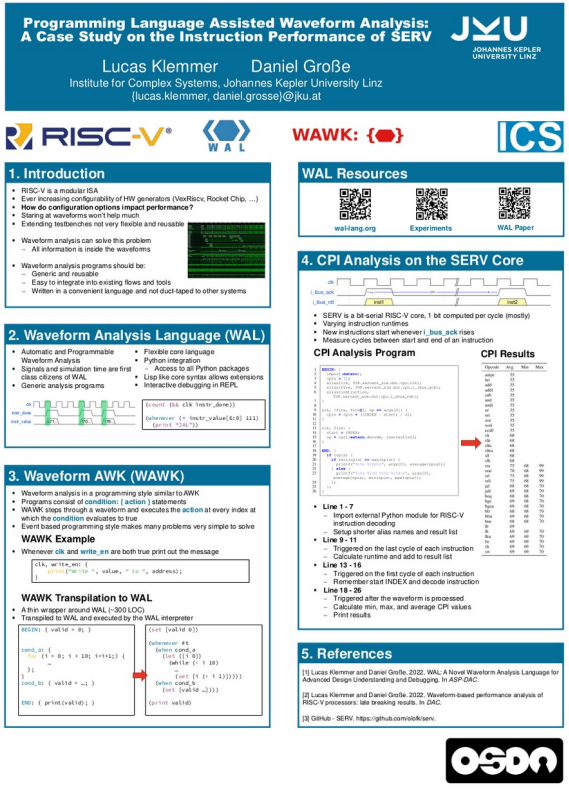
Francesco Gonnella, University of Birmingham

- Title:
  Hog 2023.1: a collaborative management tool to handle Git-based HDL repository



Lucas Klemmer, JKU Linz

- Title:
  Programming Language Assisted Waveform Analysis: A Case Study on the Instruction Performance of SERV



Programming Language Assisted Waveform Analysis: A Case Study on the Instruction Performance of SERV

Lucas Klemmer Daniel Große

Institute for Complex Systems, Johannes Kepler University Linz

{lucas.klemmer, daniel.grosse}@jku.at

1. Introduction

2. Waveform Analysis Language (WAL)

3. Waveform AWK (WAWK)

WAL Resources

4. CPI Analysis on the SERV Core

5. References

This work has been partially supported by the LIT Secure and Correct Systems Lab funded by the State of Upper Austria.

Stefan Riesenberger, TU Wien

- Title:
  Towards Power Characterization of FPGA Architectures to Enable Open-Source PowerEstimation Using Micro-Benchmarks

Towards Power Characterization of FPGA Architectures to Enable Open-Source Power Estimation Using Micro-Benchmarks

Stefan Riesenberger (stefan.riesenberger@tuhamai.tuwien.ac.at)
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TU Wien, Institute of Computer Technology
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1 Abstract

2 Problem Statement

3 Simple Benchmarking

4 Testbenches

5 Experimental Setup

6 LUT4 – Results

7 Ring-oscillator – Results

8 Conclusion

9 Future Outlook

Christian Krieg <christian@osda.ws>

Vamsi Vytla, Lawrence Berkeley National Laboratory

- Title:
  Newad: A register map automation tool for Verilog

Have a great workshop!

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